

PROCESSOR TYPE PDP-11 Family

DX11B-C0001 CODE: F WL: A
MAY-72 - PROBLEM: Glitch pulse on DX RESET line.
CORRECTION: Change wiring and the Wire List.
In-plant effectivity -03 rework immediately
Field effectivity -Rework DX11-B #11
(Time To Install And Test 6.0 Hours) (Kit Contents -FCO/Prints)

DX11B-0002 CODE: P
MAY-70 - PROBLEM: Update panel logo
CORRECTION: Add variation to drawing legend.
In-plant effectivity -Documentation change only

DX11B-0003 CODE: D WL: B
MAY-72 - PROBLEM: BLLM delay not synchronized with clock; this allowed SRVI to set before the data was changed.
CORRECTION: Synchronize BLLM with a flip-flop.
In-plant effectivity -Rework immediately

DX11B-B0004 CODE: DF
JUN-72 - PROBLEM: The inlay for the DX11-B indicator panel must be changed to reflect changes in the logic.
CORRECTION: Change silk screen; "PDPTO " becomes "NXM " and "CLR EN " becomes blank.
In-plant effectivity -03 retrofit immediately
Field effectivity -Retrofit all DX11-B's
(Time To Install And Test 1.0 Hour.) (Kit Contents -FCO/Prints And Parts)

DX11B-0005 CODE: P DD: A
AUG-72 - CORRECTION: Correct print errors and omissions.
In-plant effectivity -Documentation change only

DX11B-A0006 CODE: DF DD: B WL: C
AUG-72 - PROBLEM 1: Loss of interrupt address due to hardware clearing of DONE .
CORRECTION 1: Add INTREQ flip-flop which will not be reset until the interrupt is complete.
PROBLEM 2: INIT does not clear MNCLN .
CORRECTION 2: Change the clear input of MNCLN from DCLO to INIT
In-plant effectivity -03 retrofit immediately
Field effectivity -Retrofit all DX11-B's
(Time To Install And Test 2.0 Hours.) (Kit Contents -FCO/Prints)

DX11B-A0007 CODE: F
NOV-72 - PROBLEM: Potential noise problem in DX11-B, serial numbers 11 through 21. This is caused by wire wrap revision "P8 " routing. Unit may fail IBM 2848 diagnostic.
CORRECTION: Delete wires from TAG OUT receivers to TAG CONTROL flip-flops and replace with twisted pair. Four lines are affected; they are: ADRO, HLDO, CMDO, and SRVO .
In-plant effectivity -None
Field effectivity -Retrofit DX11-B's #11 thru #21.
(Time To Install And Test 1.0 Hour.) (Kit Contents -FCO/Prints)

DX11B-0008 CODE: P DD: C
DEC-72 - CORRECTION: Change the call-out for the IBM cable assembly. Correct print errors.
In-plant effectivity -06 documentation change only

DX11B-B0009 CODE: DF DD: D WL: D
JAN-73 - PROBLEM 1: Insufficient IBM address decoding for installation requiring more than sixteen IBM device addresses.
CORRECTION 1: Add appropriate IBM address decoding for address recognition logic.
PROBLEM 2: Maintenance clock diagnostics require back panel jumper to inhibit NPRTO from setting.
CORRECTION 2: Add gating to inhibit the setting of NPRTO when MAINTENANCE CLOCK MODE is being used.

NOTE: This FCO adds an M121 module in slot A28.
In-plant effectivity -03 retrofit immediately
Field effectivity -Retrofit all DX11-B's.
(Time To Install And Test 4.0 Hours.) (Kit Contents -FCO/Prints And Parts)

DX11B-B0010 CODE: DF DD: E WL: E
MAY-73 - PROBLEM 1: Documentation in print set in error.
CORRECTION 1: Correct print set.
PROBLEM 2: STKSTB being entered into the tumble table instead of STKSTA .
CORRECTION 2: Change tumble table input to STKSTA .
PROBLEM 3: BLLF not being cleared by DXRESET or INIT .
CORRECTION 3: Clear BLLF with DXRESET or INITPA .

NOTE: The ADD/DELETE's are as follows: DELETE D11S2 to B08U1 and B08U1 to C11M1. ADD D11S2 to C11M1.
In-plant effectivity -03 -Break-in May 23, 1973
Field effectivity -Retrofit all DX11-B's
(Time To Install And Test 2.0 Hours.) (Kit Contents -FCO/Prints)

DX11B-00011 CODE: M DD: F
AUG-73 - PROBLEM: H950-P and -Q assemblies have been modified to delete spacer #74-07789 and replace it with a thick latch modding, #12-11386. The same ECO created new variations.
CORRECTION: Add thick latch molding #12-11386 to Parts List; delete spacer #74-07789. Call for correct variations on H950-P and -Q cover panels.
In-plant effectivity -02 phase-in

DX11B-B0012 CODE: F DD: H WL: F
SEP-73 - PROBLEM 1: When BSYEN is set, the DX11 does not go off-line to the 360 when STOP is generated in the DX11.
CORRECTION 1: Add gating to unconditionally clear CUBSY when PHASE 7 is entered with STOP asserted.
PROBLEM 2: The NPRTO flag comes up intermittently when the DX11 diagnostic is run.
CORRECTION 2: Add gating to unconditionally clear NPRTO when MCLN is set.

NOTE: There are eighteen wiring add's and fourteen delete's.
In-plant effectivity -Retrofit immediately
Field effectivity -Retrofit all DX11-B's
(Time To Install And Test 1.5 Hours.) (Kit Contents -F1051 -FCO/Prints)

DX11B-00013 CODE: D DD: J
OCT-73 - PROBLEM: Phase-out of H720-E and H720-F Power Supplies; phase-in of 861 Power Control.
CORRECTION: Phase-in of H740-D Power Supply, 861 Power Control, modification to Miscellaneous Control and EPO panel addition of H722 Step-down Transformer for 230 volt operation.
In-plant effectivity -02 phase-in as parts become available.